

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	187783	(driv\$3 control) near3 circuit same ((print\$3 adj element) actuator resistor heater)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/03 19:04
L2	72219	("347").CLAS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/08/03 19:04
L3	4958	L1 and L2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/03 19:04
L4	1036	channel and decoder same shift adj register same latch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/03 19:04
L5	56	L3 and L4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/03 19:04
L6	18525	(share shared common) near4 (signal wire wiring) same channel	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/03 19:12
L7	72219	("347").CLAS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/08/03 19:12
L8	92	L6 and L7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/03 19:12

EAST Search History

L9	2037	(share shared common) adj (signal wire wiring) same channel	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/03 19:13
L10	15	L7 and L9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/03 19:19
L19	665	(347/50).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/08/03 19:28
L20	380	(347/58).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/08/03 19:25
L21	781	(347/12).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/08/03 19:25
L22	2	("6527367").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/08/03 19:28
S1	19	(("6491377") or ("6474782") or ("2002036781") or ("2074545") or ("1372966")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/08/03 18:21
S34	18	(("6491377") or ("6474782") or ("20020036781") or ("2074545") or ("1372966")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/08/03 18:21

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L15	4	(shar\$3 and wir\$3 and channel and circuit and control\$4 and print\$3).clm.	US-PGPUB	OR	ON	2007/08/03 19:23
L16	20	(shar\$3 and wir\$3 and circuit and control\$4 and print\$3).clm.	US-PGPUB	OR	ON	2007/08/03 19:23